## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE,

re the Application of

Nobuaki HASHIMOTO

Application No.: 09/486,561

Filed: February 29, 2000

For: SEMICONDUCTOR DEVICED AND METHOD THEREOF, CIRCUIT BOARD AND

ELECTRONIC INSTRUMENT

Group Art Unit: 2815

Examiner: L. Cruz

Docket No.: 105030

## **INFORMATION DISCLOSURE STATEMENT**

Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

1. An English-language translation of Japanese Application No. 10-84014 filed February 29, 2000 is attached hereto.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

Eric D. Morehouse Registration No. 38,565

JAO:CCH/mdw Date: May 29, 2001

OLIFF & BERRIDGE, PLC P.O. Box 19928 Alexandria, Virginia 22320 Telephone: (703) 836-6400 DEPOSIT ACCOUNT USE AUTHORIZATION

Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461

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Form PTO-1449 (REV. 8-83)	US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 105030		APPLICATION NO. 09/486,561			
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LC	10-84014	03/1998	Japan (English translat	ion)				
	OTHER DOCUMENTS (Incl	uding Author,	Title, Date, Pertinent Pag	es, etc.)				
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